

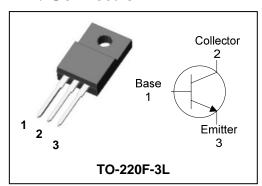


**NPN Silicon Transistor** 

#### **Features**

- Power Transistor General Purpose application
- $\bullet$  Low saturation voltage :  $V_{\text{CE(SAT)}} {=}\, 0.4 V$  Typ.
- High Voltage: V<sub>CEO</sub>=60V Min.

#### **PIN Connection**



## **Ordering Information**

Type NO.	Marking	Package Code
STC403	STC403	TO-220F-3L

### **Marking Diagram**

AUK

△YMDD

STC403

Column 1 : Manufacturer

Column 2 : Production Information
- △ : Factory Management Code

- YMDD : Date Code (Year, Month, Date)

Column 3 : Device Code

## **Absolute maximum ratings**

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	80	V
Collector-emitter voltage	$V_{\sf CEO}$	60	V
Emitter-base voltage	$V_{EBO}$	5	V
Collector current	I <sub>C</sub>	3	А
Collector power dissipation (Tc=25℃)	P <sub>C</sub>	15	W
Junction temperature	Tj	150	°C
Storage temperature	$T_{stg}$	-55~150	°C

Characteristic		Symbol	Typ.	Max.	Unit	
Thermal resistance	Junction-case	$R_{th(J-C)}$	-	8.33	°C/W	

# **Electrical Characteristics**

Charac	cteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage		BV <sub>CEO</sub>	I <sub>C</sub> =50mA, I <sub>B</sub> =0	60	-	-	V
Collector cut-off cu	urrent	I <sub>CBO</sub>	V <sub>CB</sub> =60V, I <sub>E</sub> =0	-	-	50	μА
Emitter cut-off cur	rent	I <sub>EBO</sub>	V <sub>EB</sub> =5V, I <sub>C</sub> =0	-	-	50	μΑ
DC current gain		h <sub>FE</sub> *	V <sub>CE</sub> =5V, I <sub>C</sub> =0.5A	200	-	400	-
Base-emitter on vo	oltage	V <sub>BE(ON)</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =0.5A	-	0.7	1	V
Collector-emitter s	saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =2A, I <sub>B</sub> =0.2A	-	0.4	1	V
Transition frequence	су	f <sub>T</sub>	V <sub>CB</sub> =5V, I <sub>C</sub> =0.5A	-	30	-	МН
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz	-	20	-	pF
Switching Time	Turn-on Time	T <sub>on</sub>	181 = −182 = 0.2A  DUTY CYCLE ≤ IX	-	0.65	-	
	Storage Time	$T_{stg}$		-	1.3	-	μs
	Fall Time	T <sub>f</sub>		-	0.65	-	

<sup>\*</sup> hFE rank : 200~400 Only

### **Electrical Characteristic Curves**

Fig. 1  $P_C$  - Ta

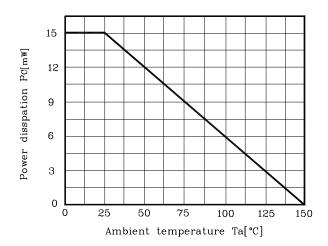


Fig. 2  $V_{CE(sat)}$  -  $I_C$ 

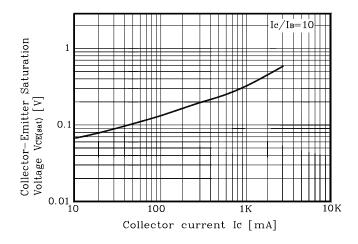


Fig. 3  $h_{\text{FE}}$ - $I_{\text{C}}$ 

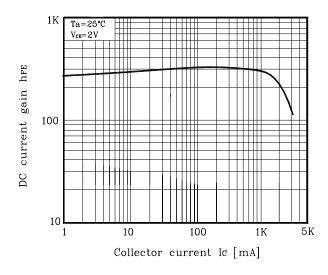


Fig. 4  $I_C$  -  $V_{CE}$ 

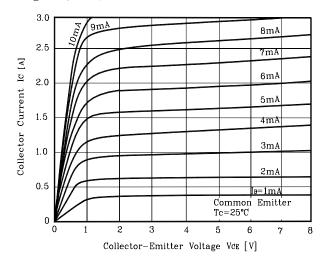
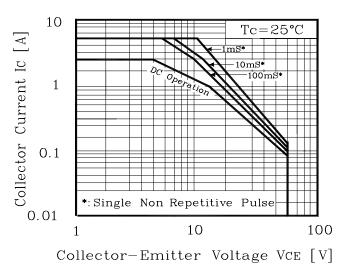
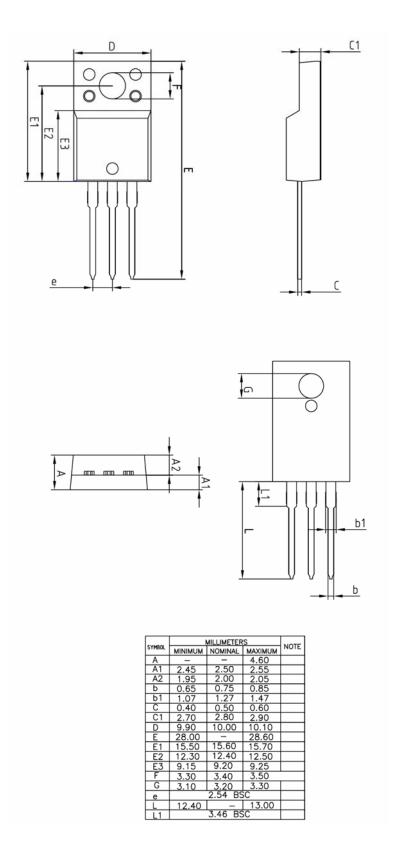


Fig. 5 Safe Operating Area



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# **Outline Dimension**



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